

INTERFERENCE

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	29	(SDIO).clm.	US-PGPUB; USPAT	OR	OFF	2006/12/12 10:47
L4	1	(SDIO and quartz and controller). clm.	US-PGPUB; USPAT	OR	OFF	2006/12/12 10:54
L6	2	(SDIO adj host adj device).clm.	US-PGPUB; USPAT	OR	OFF	2006/12/12 10:48
L7	1	(SDIO adj card adj development). clm.	US-PGPUB; USPAT	OR	OFF	2006/12/12 10:55
L9	1	(SDIO and "HS-UART" and PCMCIA).clm.	US-PGPUB; USPAT	OR	OFF	2006/12/12 10:56

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	86534	@pd>"20060709"	USPAT	OR	OFF	2006/12/12 08:37
L2	861	717/124.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 08:41
L3	51	L1 and L2	USPAT	OR	OFF	2006/12/12 08:37
L4	3876	714/25,30,31,48.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 08:37
L5	166	L1 and L4	USPAT	OR	OFF	2006/12/12 08:37
L6	4986	324/765.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 08:37
L7	147	L1 and L6	USPAT	OR	OFF	2006/12/12 08:37
L10	212	development and (UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:15
L14	29	(debug\$4 development) and (UART and PCMCIA and (SD SDIO))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 09:57
L16	14	1 and SDIO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 09:58
L17	14	((development debug\$4) with (sd memory smart) with card) and (UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:12

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L20	0	1 and ((development debug\$4) with (sd memory smart) with card) and (UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:12
L21	2	(chip core board substrate) with (PCMCIA and HS-UART and (SD memory smart))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:13
L22	0	1 and (HS-UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:13
L23	2	development and (HS-UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:14
L24	9	SDIO and (UART and PCMCIA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 10:15